

Eaton ELC Programmable Controller

Information Sheet for Crimson

Compatible Devices

• Eaton Programmable Controller

Verified Device

• ELC-PB14NNDR-4

Driver Options: The programmer selects ASCII or RTU.

If RTU, data length **MUST** be **8** bits.

Device Options: The programmer indicates the device's address.

Accessible Data

Prefix	Description	Maximum Range *	Notes
D	Holding Register	0 – 9999	1,2
М	Memory Bit	0 – 4095	2
S	Step Point/Memory Bit	0 – 1023	
Χ	External Input	0 – 255	
Υ	External Output	0 – 255	
TV	Timer Value	0 – 255	
TC	Timer Contact	0 – 255	
CV	Counter Value – 16 Bits	0 – 255	
CL	Counter Value – 32 Bits	0 – 255	
CC	Counter Contact	0 – 255	

^{* –} The Maximum Range for selections can depend on the model. The programmer is responsible for knowing what addresses are valid for the device chosen.

General note: It is very important that the programmer assign **D** tags with data types matching that of the executable code in the ELC.

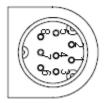
NOTE 1. The D range is not continuous for the PB.

NOTE 2. Scattered addresses above 999 are assigned to system values. The programmer is responsible for knowing which, and should not attempt to store data into those registers, without knowing the effect it will have on the operation of the PLC.

Cable Information

RS232 Serial Connection

Cable end to insert into ELC



PIN 1,2: 5V ---- DO NOT CONNECT

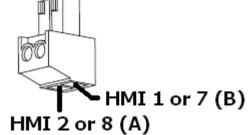
PIN 8: GND ---- HMI 3/4

PIN 4: RX ---- HMI 5 (TX)

PIN 5: TX ---- HMI 2 (RX)

RS485 Serial Connection





Connection to Programming Cable

RS232

9 Pin D	Signal	RJ
2	RxD - TxD	5
3	TxD – RxD	2
5	OV	3 or 4