

## IDEC Micro 3 / ONC / MicroSmart

Information Sheet for Crimson v2.0

**Compatible Devices** 

- Idec Micro3
- Idec Open Net Controller
- Idec Micro Smart IDEC Processor FC4A-C24R2 (All in One Type) and Communication Adapter FC4A-PC2 (RS485)

Verified Device

• Idec Micro3 FC2A

Driver Options None

Device Options The programmer sets the target addresses of the Idec PLC's to be accessed.

## Important Information

The values of Min and Max displayed in the selection dialog box do not necessarily reflect the actual number of accessible points for a particular PLC. The programmer must be aware of the limits of the device, which is to be connected to the HMI.

No attempt is made to verify any value being written. It is the responsibility of the programmer to ensure valid data are written.

## Accessible Data

The following registers are accessible:

Mnemonic	Action	Data Type	Notes
CC	Counter-Current	Word	1
СР	Counter-Preset	Word	
D	Data Register	Word	
Ι	Input Bits	1 Bit	2
IB	Input Bytes	8 Bits	3
М	Internal Memory Bits	1 Bit	2
MB	Internal Memory Bytes	8 Bits	3

Q	Output Bits	1 Bit	2
QB	Output Bytes	8 Bits	3
R	Shift Register Bits	1 Bit	
RB	Shift Register Bytes	8 Bits	3
S8	Special Register Bits	1 Bit	2,4
SB8	Special Register Bytes	8 Bits	3,4
TC	Timer-Current Value	Word	1
TP	Timer-Preset Value	Word	

Note 1. CC and TC are READ-ONLY Registers.

Note 2. These Bit addresses comprise a decimal byte number, and an octal bit number. All other addresses are entered as decimal numbers.

Note 3. Byte addresses are the lowest of 8 consecutive bits, and are decimal numbers. For example, IB12 will access I120 to I127.

Note 4. Special Registers are not available on the Micro3 and Micro3C. When using the ONC PLC, the programmer chooses only the offset from 8000. The driver automatically inserts the 8 before the value.

Cable Information

G3 RS-232 Port	IDEC (8 way male mini-DIN)
5	4
2	3
4	7
G3 RS-485 Port	IDEC (8 way male mini-DIN)
1	1
2	2
6	7
3 and 8	NC
4 and 7	NC

