

Mitsubishi Q Series TCP/IP Master Driver

Information Sheet for Crimson v2.0

Compatible Devices

Mitsubishi Q or QnA* series PLC fitted with appropriate Ethernet communications module.

Verified Device

Mitsubishi Q00J CPU via QJ71E71-100 Module Mitsubishi Q2ASH CPU via A1SJ71QE71N-T Module *

Supported Communication Modes

Only Binary Code communications are supported.

Accessible Data

Prefix	Description	Access
Х	Input	Read / Write
Y	Output	Read / Write
М	Internal Relay	Read / Write
F	Annunciator	Read / Write
В	Link Relay	Read / Write
D	Data Register	Read / Write
W	Link Register	Read / Write
Т	Timer Current Value	Read / Write
С	Counter Current Value	Read / Write

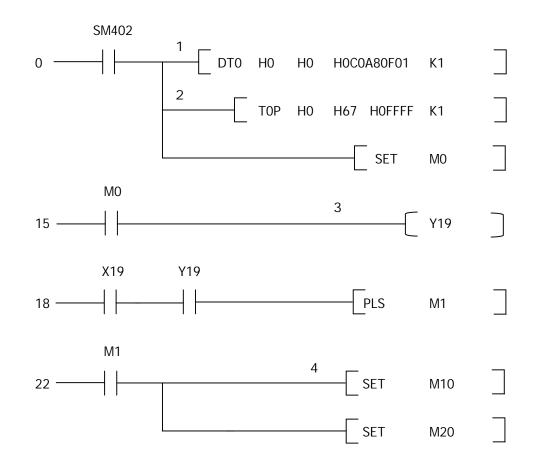
Cable Information

Standard 10-Base-T Ethernet Cable

* Note: Some compatible devices require a ladder program in order to achieve successful Ethernet communications. Sample programs can be found on the following page.

Sample Ladder Program (MELSEC – QnA Series)

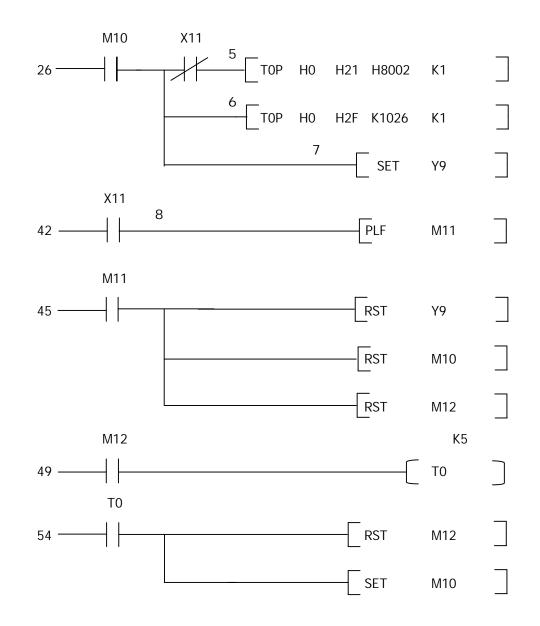
Initial Processing



- 1) Set the IP Address to "192.168.15.1"
- 2) Enable Communication Instruction during STOP for all connections.
- **3**) Turn on Initial Request signal.
- 4) After Initial Complete signal, set the TCP/IP port to WAIT-OPEN.

Sample Ladder Program (MELSEC – QnA Series) continued

TCP/IP Open Processing



- 5) Set Connection 2 protocol.
- 6) Set Connection 2 local port.
- 7) Turn on Open Request.
- 8) When Open Complete signal turns off, turn Open Request off once and repeat step 7 after 0.5 seconds.